

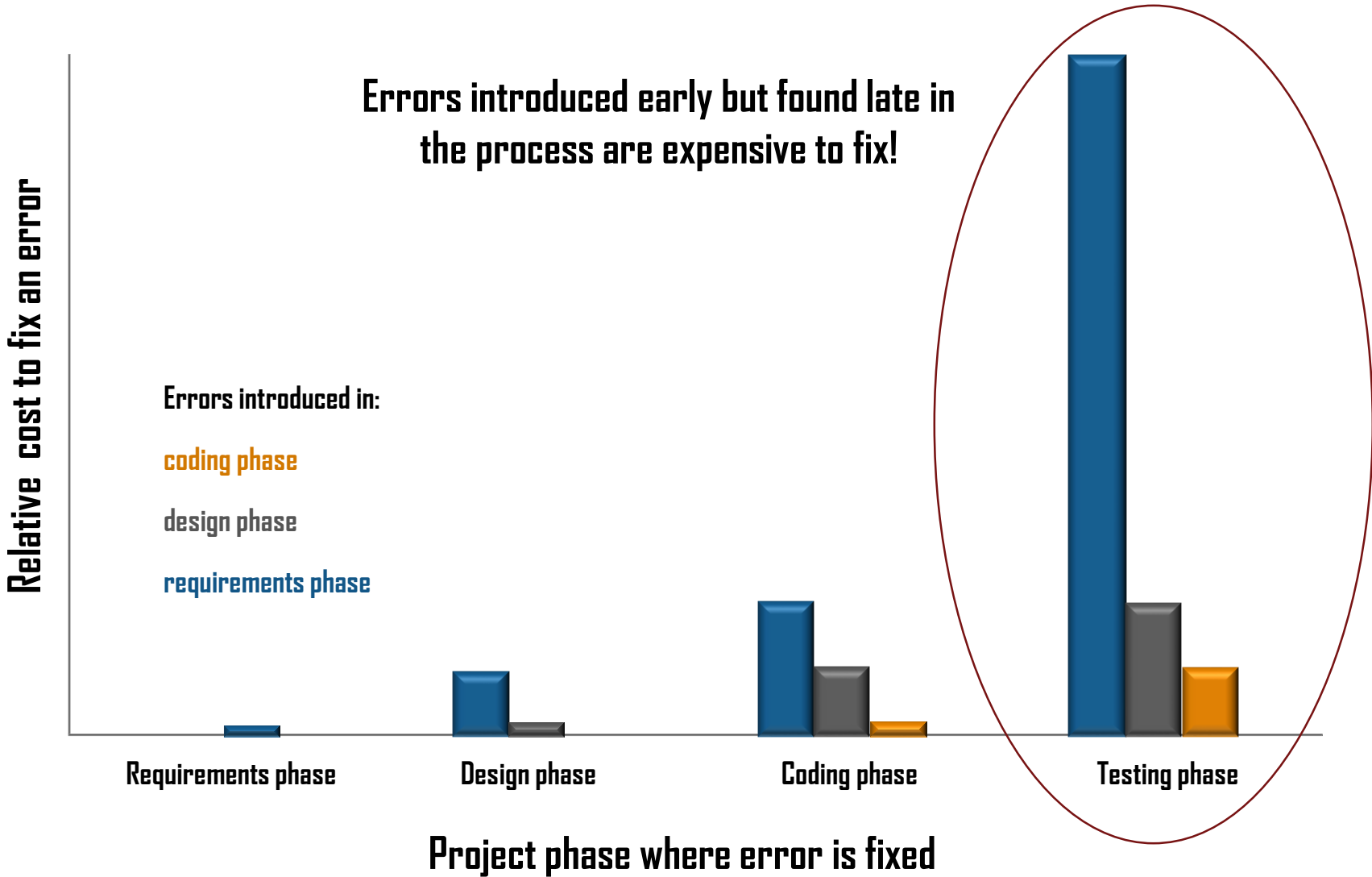
dSPACE aMBD

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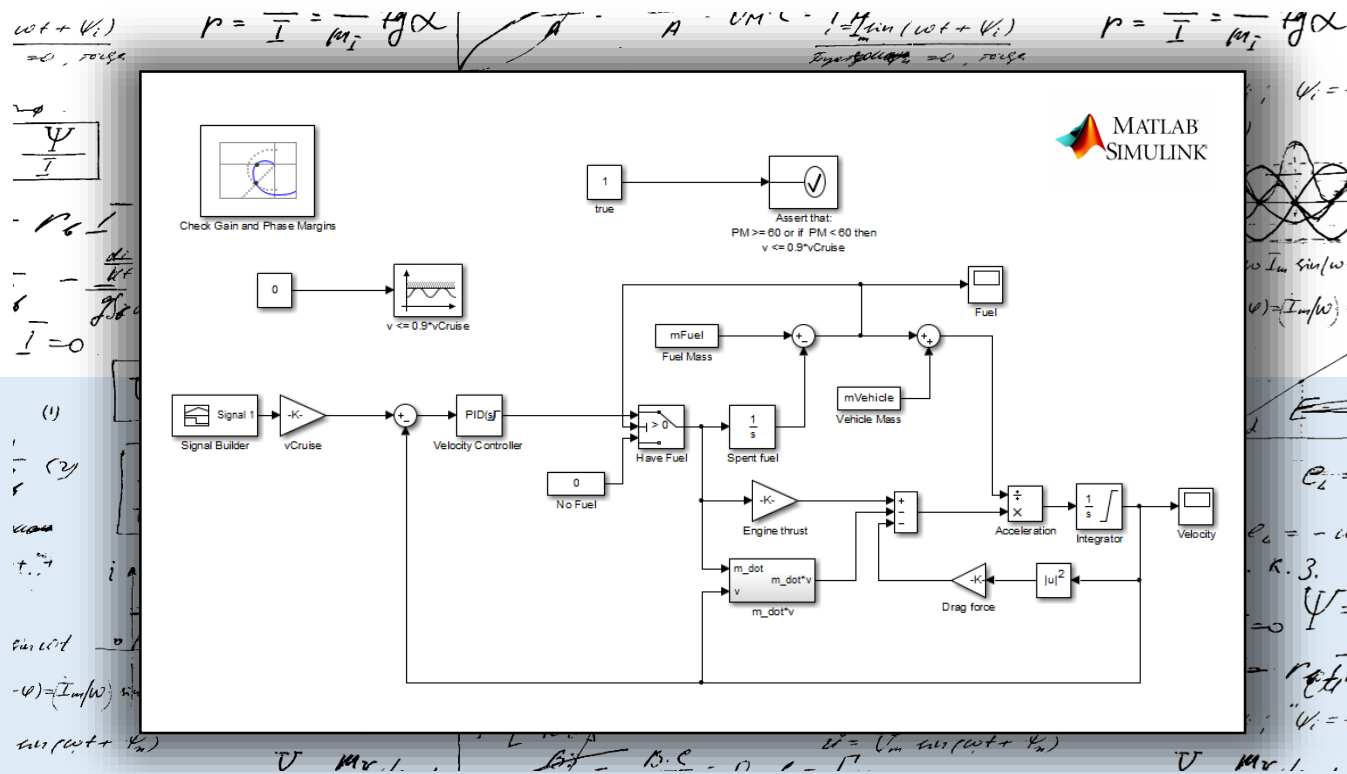




# KOLKO STOJÍ CHYBA?



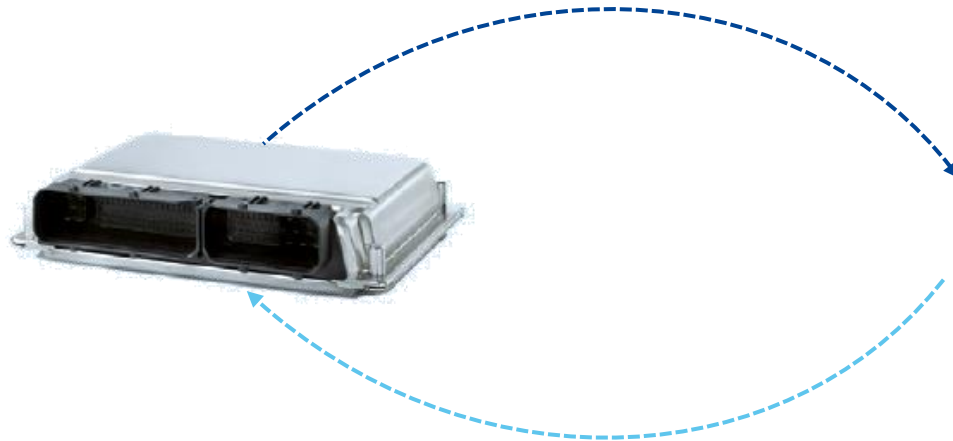
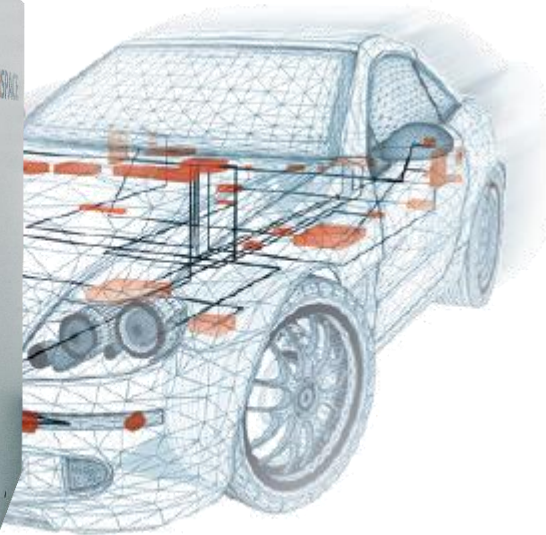
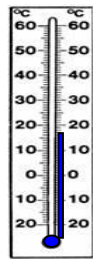
Source: Return on Investment for Independent Verification & Validation, NASA, 2004.



Model je spustiteľná špecifikácia. Matematický opis, ktorý dáva inžinierom rôzne pohľady na správanie systému. Vytvorenie modelu vyžaduje niekoľko krokov ako sú identifikácia systému, modelovanie rovnicami, vytvorenie diagramu a pod. Následne môže byť model využitý na generovanie kódy a testovacie scenáre MIL, SIL, PIL, HIL...

# HARDWARE-IN-THE LOOP

ECU si myslí, že naozaj jazdí





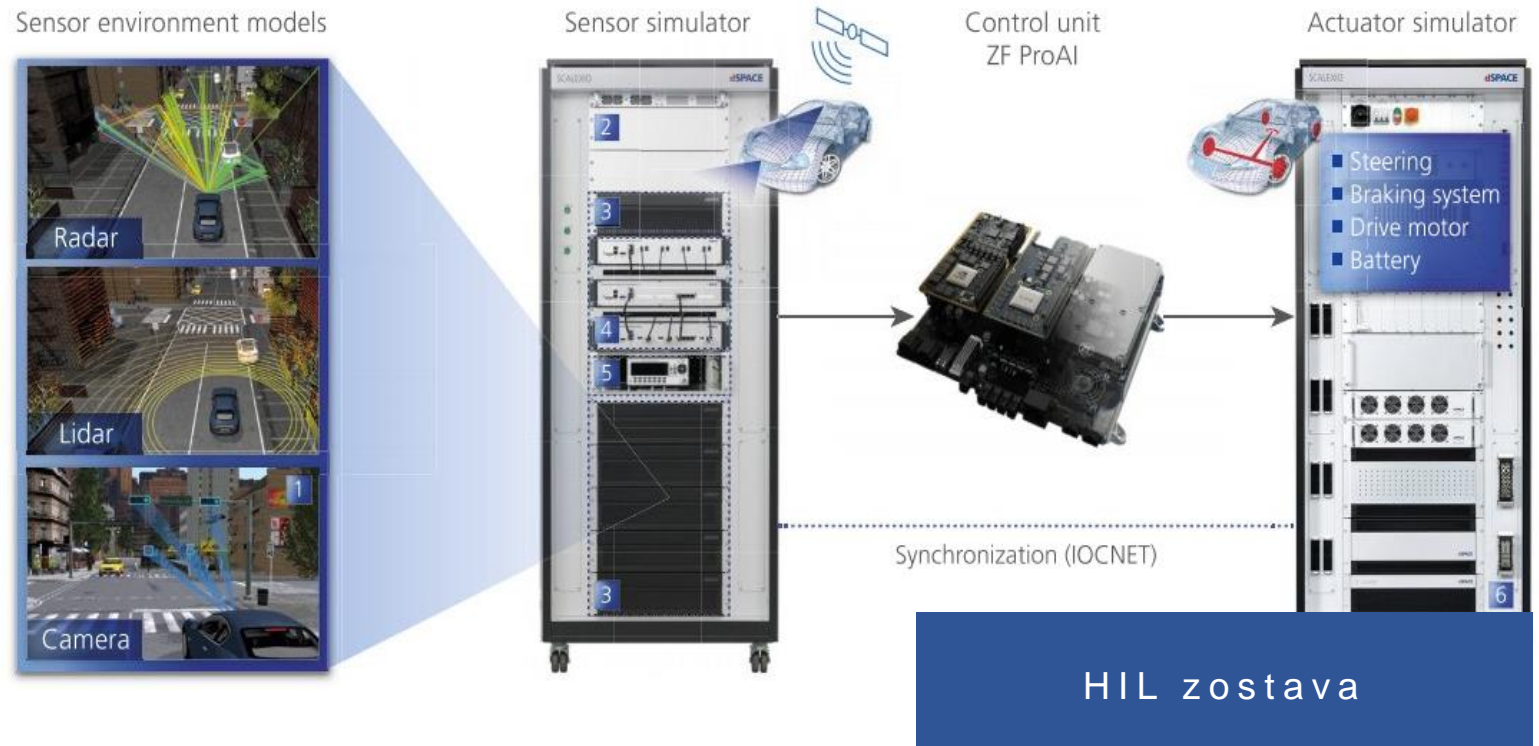




## REALISTICKÁ SIMULÁCIA SENZOROV

"We rely on the powerful dSPACE tool chain to validate the AI-based control unit of our autonomous technology platform as early as possible and in combination with the sensors and actuators." Oliver Maschmann, ZF

# AI IN-THE-LOOP





# ROZŠÍRENÁ REALITA

VEHICLE-IN-THE-LOOP

"We take the vehicle-in-the-loop (VIL) approach when validating ADAS/AD functions because it gives us the combined advantages of real and virtual test drives. The robust real-time system from dSPACE enables us to implement this test method in the vehicle and achieve results that are extremely precise and realistic."

Teaseung Kim, Hyundai MOBIS



The vehicle is controlled only by its advanced driver assistance systems – ADAS.



**VIRTUÁLNA  
REALITA**

Senzory zachytávajú informácie z virtuálneho sveta...



**SKUTOČNÝ SVET**

... a použijú ich na ovládanie reálneho vozidla.





Picture credits: © Hyundai MOBIS

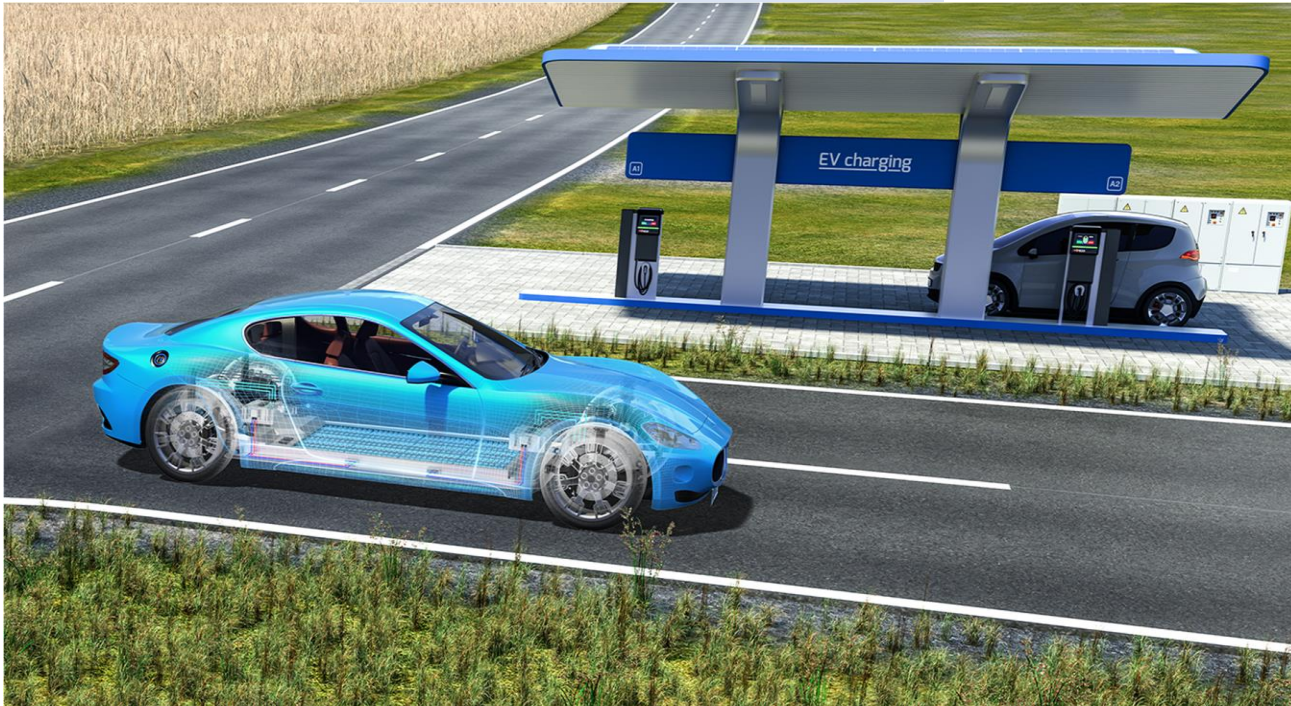
MODULÁRNY RT SYSTÉM DO VOZIDIEL

# SCALEXIO AutoBox



# ELEKTROMOBILITA

KOMPLETNÝ VÝVOJ A TESTOVANIE



VÝVOJ



SIMULÁCIA



AUTO CODING



TESTOVANIE



# CONCEPT\_ONE

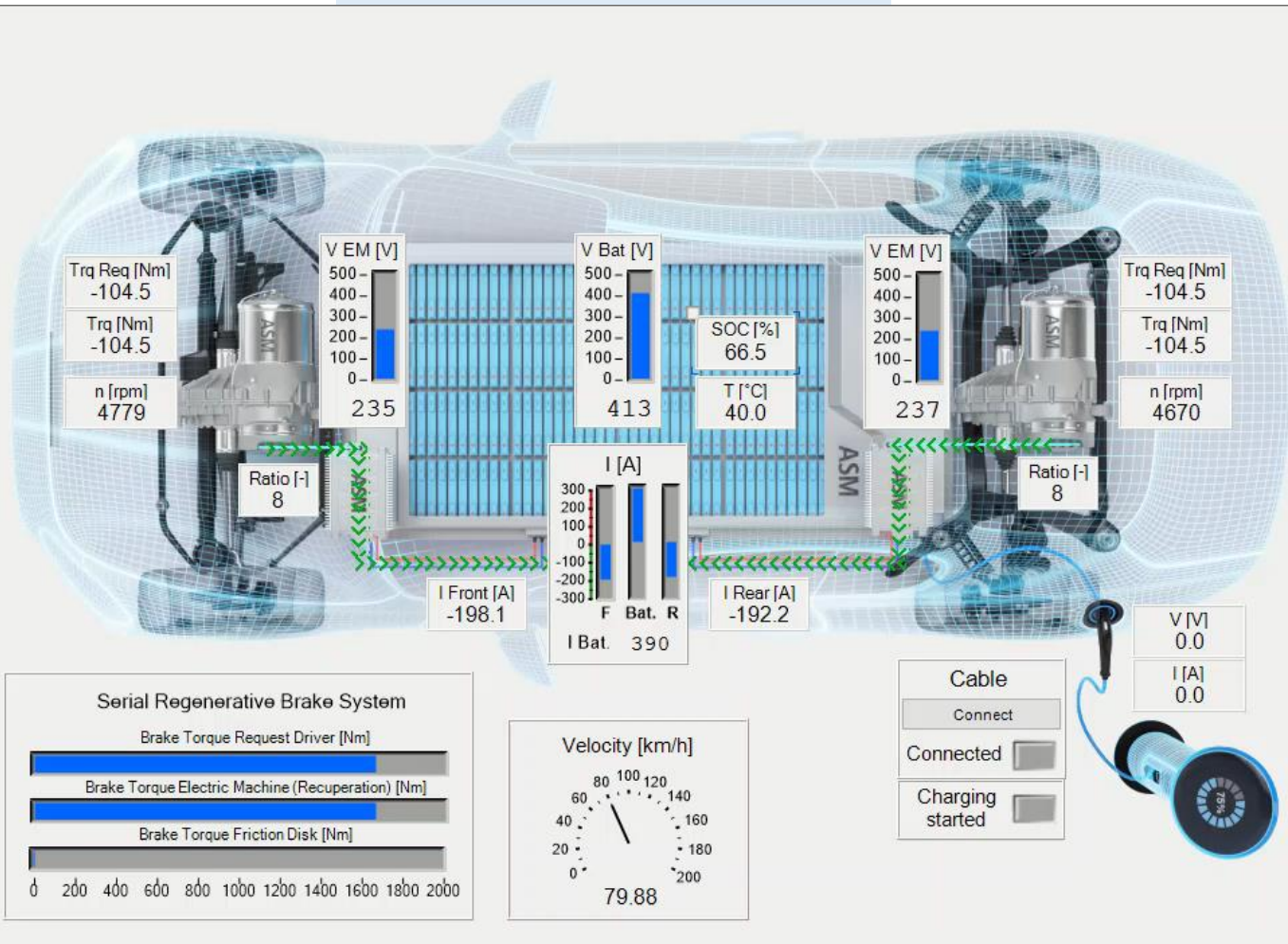


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“ Working with the MicroAutoBox is simple and straightforward, allowing control engineers to focus on controller development without worrying about the underlying code.”

Tomislav Šimunić, head of the vehicle dynamics team, Rimac Automobili

# SIMULÁCIA



**Maneuver Control**

Maneuver State  
**Run**

Maneuver Time [s]  
1979

Start Maneuver

Stop Maneuver

Reset Vehicle

Disable Battery Reset

**Variant**

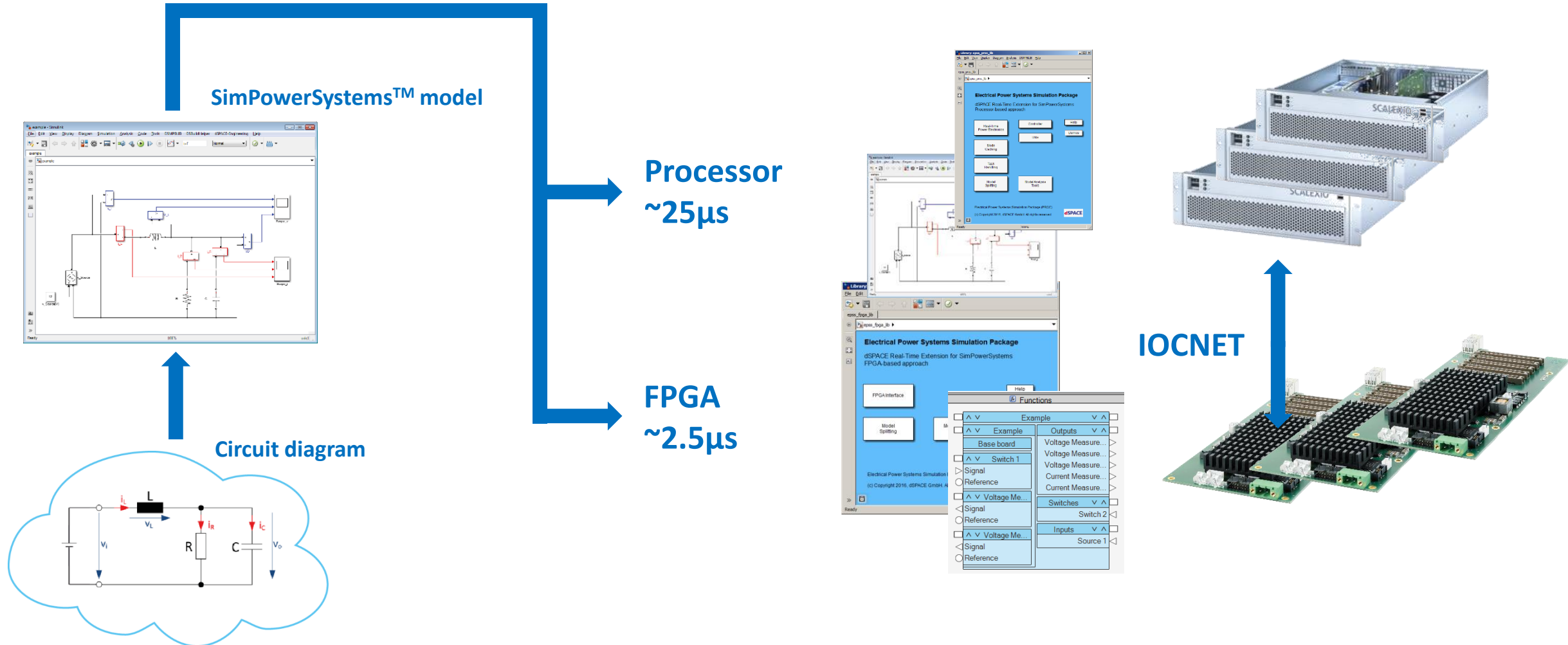
Drivetrain  
**AWD**

Tire Model  
**TMEasy**



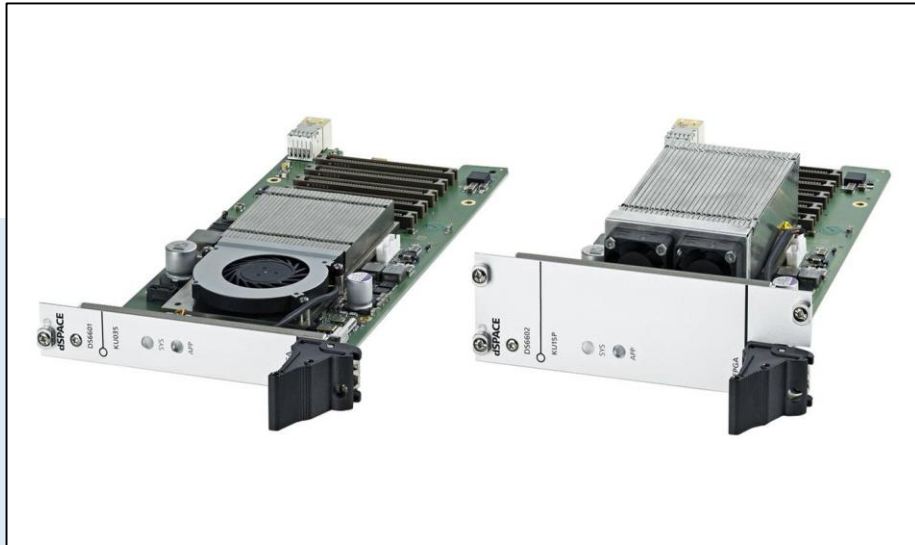


# Electrical Power Systems Simulation (EPSS) Package



# FPGA BASE BOARDS

DS6601 / DS6602



Vysokovýkonné FPGA karty vybavené najnovšou FPGA technológiou pre splnenie najprísnejších požiadaviek v širokom rozsahu aplikácií ako sú elektrické pohony, hybridné vozidlá, výkonová elektronika.

U ž í v a t e ľ o m   p r o g r a m o v a t e ľ n é   F P G A

# Embedded Success

VÝVOJ A TESTOVANIE MECHATRONICKÝCH  
SYSTÉMOV





**ĎAKUJEM**

**TCC 2019**

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