

# SYMBOLIC ANALYSIS OF LINEAR ELECTRIC CIRCUITS

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## Abstract

In present days there exist lots of programs such as PSPICE, TINA, which enable to solve circuits numerically. But sometimes it is useful to use program which offers symbolic results for solved circuit. The paper is devoted to the creation of application program for symbolic analysis of linear circuit system containing independent and dependent sources of DC voltage and/or current. This program is based on nodal analysis.

## 1 Principles of Nodal Analysis and its Matrix Formulation

Nodal analysis is method for solving linear electric circuits. Unknown quantities are nodal voltages. A nodal voltage is a voltage between a node pair, which is formed by a nonreference node and a reference node. A node is a point of connection of two or more circuit elements. A word formulation of nodal analysis procedure is following [1]:

The first step is determination the number of nodes in the circuit. One of the nodes is selected as the reference node and a node voltage is assigned to each nonreference node. For an  $N_u$ -node circuit there are  $N_u - 1$  nodal voltages, so  $N_u - 1$  linearly independent equations are required to solve for the nodal voltages.

The second step is a writing a constraint equation for each independent or dependent voltage source in the circuit in terms of the assigned nodal voltages using Kirchhoff's voltage law (KVL). If the  $k$ th voltage source  $u_k^{(s)}$  is connected between a nonreference node  $p$  and the reference node, then the nodal voltage  $u_p^{(n)}$  is given by source voltage accounting for the polarities, and the constraint equation takes the following form:

$$u_p^{(n)} = \pm u_k^{(s)} \quad (1)$$

If the  $k$ th voltage source  $u_k^{(s)}$  is connected between a nonreference node  $p$  and a nonreference node  $q$ , then the difference of the nodal voltages  $u_p^{(n)}$  and  $u_q^{(n)}$ , assigned to these nonreference nodes, is given by source voltage accounting for the polarities, and the constraint equation takes the form:

$$u_p^{(n)} - u_q^{(n)} = \pm u_k^{(s)} \quad (2)$$

Each constraint equation represents one of the required linearly independent equations. For each dependent voltage source it is necessary to express the controlling variable in terms of the nodal voltages. For a circuit containing  $N_{NZ}$  voltage sources, with  $N_{NZ}^{(1nonref)}$  connected between a nonreference node and the reference node and  $N_{NZ}^{(2nonref)}$  connected between two nonreference nodes, a number of the constraint equations is  $N_{NZ} = N_{NZ}^{(1nonref)} + N_{NZ}^{(2nonref)}$  and these equations yield  $N_{NZ}$  linearly independent equations.

The remaining  $N_u - 1 - N_{NZ}$  linearly independent equations must be formulated by applying Kirchhoff's current law (KCL) at each of  $N_u - 1 - N_{NZ} - N_{NZ}^{(2nonref)}$  nonreference nodes not connected to a voltage source, and at each of  $N_{NZ}^{(2nonref)}$  supernodes, in case that only one voltage source is connected to each node. A supernode is a closed surface that bounded two nonreference nodes connected by a voltage source.

Because MATLAB (MATrix LABoratory) is a tool for matrix solving of problems, it is useful to create a matrix formulation of nodal analysis.

We assume a proper linear electric circuit containing  $N_u$  nodes and  $N_v$  branches. Let us consider that a resistor with resistance  $R_k \neq 0 \wedge R_k \neq \infty$ , an ideal current source (independent or dependent) with a current  $i_k^{(S)}$  and an ideal voltage source with a voltage  $u_k^{(S)}$  are connected in a branch  $v_k$  ( $k = 1, 2, \dots, n_v$ ) with nodes  $p$  and  $q$  (Figure 1).

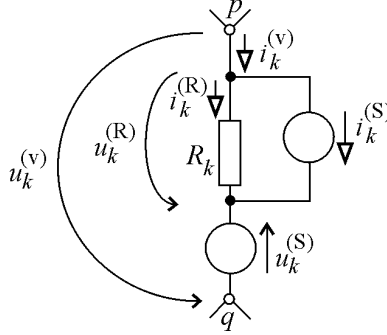


Figure 1: The  $k$ th circuit branch connected to the node  $p$  and the node  $q$

KCL written in the matrix form is [2]:

$$\mathbf{A} \mathbf{I}^{(v)} = \mathbf{0} \quad (3)$$

where  $\mathbf{I}^{(v)}$  is a matrix of branch currents  $i_k^{(v)}$  of order  $(N_v, 1)$ ,  $\mathbf{A}$  being the incidence matrix of order  $(N_u - 1, N_v)$  - a matrix with elements  $a_{mn}$  which express incidence between nodes and branches of circuit and their value is  $\{+1, -1, 0\}$ .

The branch current  $\mathbf{I}^{(v)}$  can be expressed (Figure 1):

$$\mathbf{I}^{(v)} = \mathbf{I}^{(R)} + \mathbf{I}^{(S)} \quad (4)$$

where  $\mathbf{I}^{(R)}$  is a matrix of resistor currents  $i_k^{(R)}$  of order  $(N_v, 1)$ ,  $\mathbf{I}^{(S)}$  being a matrix of current source currents  $i_k^{(S)}$  of order  $(N_v, 1)$ .

The resistor current  $\mathbf{I}^{(R)}$  will be:

$$\mathbf{I}^{(R)} = \mathbf{G} (\mathbf{U}^{(v)} + \mathbf{U}^{(S)}) \quad (5)$$

where  $\mathbf{G}$  is a diagonal matrix of resistor conductance of order  $(N_v, N_v)$ ,  $\mathbf{U}^{(v)}$  being a matrix of branch voltages of order  $(N_v, 1)$ ,  $\mathbf{U}^{(S)}$  being a matrix of voltage source voltages of order  $(N_v, 1)$ .

Each branch voltage can be expressed in terms of a nodal voltage [1]:

$$\mathbf{U}^{(v)} = \mathbf{A}^T \mathbf{U}^{(n)} \quad (6)$$

where  $\mathbf{A}^T$  is a transpose matrix to a matrix  $\mathbf{A}$ ,  $\mathbf{U}^{(n)}$  being a matrix of nodal voltages of order  $(N_u - 1, 1)$ .

Substituting equations (4), (5) and (6) into equation (3), we obtain:

$$\mathbf{A} \mathbf{G} \mathbf{A}^T \mathbf{U}^{(n)} + \mathbf{A} \mathbf{G} \mathbf{U}^{(S)} + \mathbf{A} \mathbf{I}^{(S)} = \mathbf{0} \quad (7)$$

The equation (7) represents a formulation of nodal equations in terms of incidence matrix  $\mathbf{A}$  for electric circuits containing current and/or voltage sources (independent and dependent).

Matrix  $\mathbf{U}^{(S)}$  in the equation (7) takes the following form:

$$\mathbf{U}^{(S)} = \mathbf{U}^{(IVS)} + \mathbf{U}^{(CCVS)} + \mathbf{U}^{(VCVS)} \quad (8)$$

where  $\mathbf{U}^{(IVS)}$  is a matrix of voltages independent voltage sources with a positive sign if its polarity is not oriented corresponding to a branch orientation,  $\mathbf{U}^{(CCVS)}$  being a matrix of voltages of current-controlled voltage sources (CCVS),  $\mathbf{U}^{(VCVS)}$  being a matrix of voltages of voltage-controlled voltage sources (VCVS).

Matrix  $\mathbf{U}^{(CCVS)}$  in the equation (8) is following:

$$\mathbf{U}^{(CCVS)} = \mathbf{V}^{(CCVS)}\mathbf{I}^{(v)} \quad (9)$$

where  $\mathbf{V}^{(CCVS)}$  is a matrix of coupling coefficients between voltages CCVS and controlling currents with a positive sign if its polarity is not oriented corresponding to an orientation of branch in which is the source, and simultaneously a polarity of controlling current is corresponding to an orientation of branch in which it is placed.

Matrix  $\mathbf{U}^{(VCVS)}$  in the equation (8) is following:

$$\mathbf{U}^{(VCVS)} = \mathbf{V}^{(VCVS)}\mathbf{U}^{(v)} \quad (10)$$

where  $\mathbf{V}^{(VCVS)}$  is a matrix of coupling coefficients between voltages VCVS and controlling voltages with a positive sign if its polarity is not oriented corresponding to an orientation of branch in which is the source, and simultaneously a polarity of controlling voltage is oriented corresponding to an orientation of branch in which it is placed.

Matrix  $\mathbf{I}^{(s)}$  in the equation (7) is following:

$$\mathbf{I}^{(s)} = \mathbf{I}^{(ICS)} + \mathbf{I}^{(CCCS)} + \mathbf{I}^{(VCCS)} \quad (11)$$

where  $\mathbf{I}^{(ICS)}$  is a matrix of currents independent current sources with a positive sign if its polarity is oriented corresponding to a branch orientation,  $\mathbf{I}^{(CCCS)}$  being a matrix of currents of current-controlled current sources (CCCS),  $\mathbf{I}^{(VCCS)}$  being a matrix of currents of voltage-controlled current sources (VCCS).

Matrix  $\mathbf{I}^{(CCCS)}$  in the equation (11) is following:

$$\mathbf{I}^{(CCCS)} = \mathbf{V}^{(CCCS)}\mathbf{I}^{(v)} \quad (12)$$

where  $\mathbf{V}^{(CCCS)}$  is a matrix of coupling coefficients between currents CCCS and controlling currents with a positive sign if its polarity is oriented corresponding to an orientation of branch in which is the source, and simultaneous a polarity of controlling current is oriented corresponding to an orientation of branch in which it is placed.

Matrix  $\mathbf{I}^{(VCCS)}$  in the equation (11) is following:

$$\mathbf{I}^{(VCCS)} = \mathbf{V}^{(VCCS)}\mathbf{U}^{(v)} \quad (13)$$

where  $\mathbf{V}^{(VCCS)}$  is a matrix of coupling coefficients between currents VCCS and controlling currents with a positive sign if its polarity is oriented corresponding to an orientation of branch in which is the source, and simultaneous a polarity of controlling current is oriented corresponding to an orientation of branch in which it is placed.

Substituting equations (8) up to (13) into equation (7) we obtain the matrix formulation of nodal equations for linear circuits having independent and/or dependent sources:

$$\mathbf{G}^{*(n)}\mathbf{U}^{(n)} = \mathbf{I}^{(n)} \quad (14)$$

where  $\mathbf{G}^{*(n)}$  is a node conductance matrix of order  $(N_u - 1, N_u - 1)$ ,  $\mathbf{I}^{(n)}$  being a column matrix of short-circuiting currents produced by the current sources and voltage sources of order  $(N_u - 1, 1)$ .

- The obtained system of the nodal equations (14) must be treated for ideal current and voltage sources by following way.

An internal resistance of an ideal current source that is connected in the  $k$ th circuit branch is infinitely large and so its internal conductance is equal zero ( $g_{kk} \rightarrow 0$ ).

An internal resistance of an ideal voltage source that is connected in the  $k$ th circuit branch is equal zero and so its internal conductance is infinitely large ( $g_{kk} \rightarrow \infty$ ).

For that reason it is necessary to arrange the system of nodal equation (14). If a voltage source is connected between nonreference node  $p$  and reference node, a limit for  $g_{kk} \rightarrow \infty$  of the left-hand side of equation written for nonreference node  $p$  must be found

$$\lim_{g_{kk} \rightarrow \infty} \left( \sum_{j=1}^{N_u-1} \frac{g_{pj}^{*(n)}}{g_{kk}} u_j^{(n)} \right) \quad (15)$$

as well as a limit for  $g_{kk} \rightarrow \infty$  of right-hand side of equation written for nonreference node  $p$

$$\lim_{g_{kk} \rightarrow \infty} \frac{i_p^{(n)}}{g_{kk}} \quad (16)$$

and the constraint equation in the form (1) will be obtained by this arrangement.

If a voltage source is connected between a nonreference node  $p$  and a nonreference node  $q$ , a limit for  $g_{kk} \rightarrow \infty$  of the both sides of equation written for nonreference node  $p$  must be found and the constraint equation in the form (2) will be obtained by this arrangement; but one more equation is needed, which will be provided by a nodal equation for supernode  $p - q$  in such a way, that we add the equation for nonreference node  $p$  and the equation for nonreference node  $q$

$$\sum_{j=1}^{N_u-1} (g_{pj}^{*(n)} + g_{qj}^{*(n)}) u_j^{(n)} = i_p^{(n)} + i_q^{(n)} \quad (17)$$

If any coefficient  $g_{pj}^{*(n)}$  in the equation (17) includes a term  $\pm g_{kk}$  (independent voltage source) or a term  $\pm \rho_k g_{kk} g_{ll}$  (CCVS) or a term  $\pm \alpha_k g_{kk}$  (VCVS), then a coefficient  $g_{qj}^{*(n)}$  includes a term  $\mp g_{kk}$  or a term  $\mp \rho_k g_{kk} g_{ll}$  or a term  $\mp \alpha_k g_{kk}$ , and there is no any coefficient with term  $g_{kk}$  in the equation (17).

## 2 Results

Two selected circuits will be presented as example of nodal analysis by using the program that generates the system of symbolic nodal equations. The first one is a circuit having independent sources only (Figure 1 left) and the second one is a circuit having independent and dependent sources (Figure 1 right).

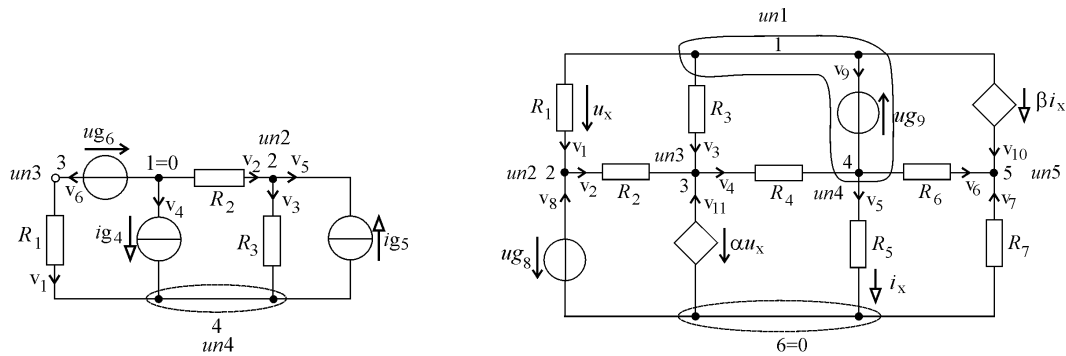


Figure 1: An example of circuit having independent sources only (left) and a circuit having dependent sources (right)

After assigning an input data (parameters of the circuit elements, the incidence matrix and the reference node) the program generates a system of symbolic nodal equations. This system is

subsequently solved and the nodal voltages are obtained in numeric form. The branch voltages and the branch currents are obtained in both symbolic and numeric form.

Referring to Figure 1, the program generates the system of equations (Figure 2 left), which consists of one constraint equation and two nodal equations for non reference nodes 2 and 4. For given values of circuit parameters ( $R_1 = 20\Omega, R_2 = 4\Omega, R_3 = 12\Omega, ig_4 = 0.3A, ig_5 = 0.9A, ug_6 = 12V$ ) the program computes numeric values of nodal voltages (Figure 2 right).

Constraint equations:

$$un3 = ug6$$

Nodal equations for non reference nodes:

$$\text{node 2: } (g_2+g_3)*un_2-g_3*un_4-ig_5 = ig_5$$

$$\text{node 4: } -un_2*g_3-g_1*un_3+(g_1+g_3)*un_4-ig_4+ig_5 = ig_4-ig_5$$

Nodal voltages - numeric values:

$$un_2 = 3.200000 \text{ V}$$

$$un_3 = 12.000000 \text{ V}$$

$$un_4 = 2.000000 \text{ V}$$

Figure 2: Symbolic equations generated by program for circuit having independent sources (left) and numeric values of nodal voltages (right)

The obtained results of the branch voltages (Figure 3 left) and the branch currents (Figure 3 right) for circuit having independent sources and the values of circuit element parameters are given in Figure 3.

Branch voltages:

$$uv_1 = un_3-un_4 = 10.000000 \text{ V}$$

$$uv_2 = -un_2 = -3.200000 \text{ V}$$

$$uv_3 = un_2-un_4 = 1.200000 \text{ V}$$

$$uv_4 = -un_4 = -2.000000 \text{ V}$$

$$uv_5 = un_2-un_4 = 1.200000 \text{ V}$$

$$uv_6 = -un_3 = -12.000000 \text{ V}$$

Branch currents:

$$iv_1 = (un_3-un_4)*g_1 = 0.500000 \text{ A}$$

$$iv_2 = -g_2*un_2 = -0.800000 \text{ A}$$

$$iv_3 = (un_2-un_4)*g_3 = 0.100000 \text{ A}$$

$$iv_4 = ig_4 = 0.300000 \text{ A}$$

$$iv_5 = -ig_5 = -0.900000 \text{ A}$$

$$iv_6 = iv_1 = 0.500000 \text{ A}$$

Figure 3: The branch voltages (left) and the branch currents (right) for circuit having independent sources

Referring to Figure 1 (right), the program generates the system of equations (Figure 4 left), which consists of three constraint equations and two nodal equations, one of them is for a nonreference node 5 and the other one is for a supernode 1-4. For given values of circuit parameter ( $R_1 = R_2 = R_3 = R_4 = R_5 = R_6 = R_7 = 1k\Omega, ug_8 = 12V, ug_9 = 6V, ig_{10} = \beta i_x = 2i_x A, ug_{11} = \alpha u_x = 2u_x V$ ) the program computes the following numeric values of nodal voltages (Figure 4 right):

Constraint equations:

$$un_2 = ug_8$$

$$-alfa_1*un_1+alfa_1*un_2+un_3 = 0$$

$$-un_1+un_4 = ug_9$$

Nodal equations for non reference nodes:

$$\text{node 5: } (-beta_1*g_5-g_6)*un_4+(g_6+g_7)*un_5 = 0$$

Nodal equations for supernodes:

$$\text{supernode 1-4: } (g_1+g_3)*un_1-g_1*un_2+(-g_3-g_4)*un_3+(beta_1*g_5+g_4+g_5+g_6)*un_4-g_6*un_5 = 0$$

Nodal voltages - numeric values:

$$un_1 = -38.000000 \text{ V}$$

$$un_2 = 12.000000 \text{ V}$$

$$un_3 = -100.000000 \text{ V}$$

$$un_4 = -32.000000 \text{ V}$$

$$un_5 = -48.000000 \text{ V}$$

Figure 4: Symbolic equations generated by program for circuit having dependent sources (left) and numeric values of nodal voltages (right)

The obtained results of the branch voltages (Figure 5 left) and the branch currents (Figure 5 right) for circuit having dependent sources and the given values of circuit element parameters are in Figure 5.

Branch voltages:

$uv1 = un1 - un2 = -50.000000 \text{ V}$   
 $uv2 = un2 - un3 = 112.000000 \text{ V}$   
 $uv3 = un1 - un3 = 62.000000 \text{ V}$   
 $uv4 = un3 - un4 = -68.000000 \text{ V}$   
 $uv5 = un4 = -32.000000 \text{ V}$   
 $uv6 = un4 - un5 = 16.000000 \text{ V}$   
 $uv7 = -un5 = 48.000000 \text{ V}$   
 $uv8 = -un2 = -ug8 = -12.000000 \text{ V}$   
 $uv9 = un1 - un4 = -ug9 = -6.000000 \text{ V}$   
 $uv10 = un1 - un5 = 10.000000 \text{ V}$   
 $uv11 = -un3 = -2 * uv1 = 100.000000 \text{ V}$

Branch currents:

$iv1 = (un1 - un2) * g1 = -0.050000 \text{ A}$   
 $iv2 = (un2 - un3) * g2 = 0.112000 \text{ A}$   
 $iv3 = (un1 - un3) * g3 = 0.062000 \text{ A}$   
 $iv4 = (un3 - un4) * g4 = -0.068000 \text{ A}$   
 $iv5 = un4 * g5 = -0.032000 \text{ A}$   
 $iv6 = (un4 - un5) * g6 = 0.016000 \text{ A}$   
 $iv7 = -un5 * g7 = 0.048000 \text{ A}$   
 $iv8 = -iv1 + iv2 = 0.162000 \text{ A}$   
 $iv9 = -iv1 - iv3 - iv10 = 0.052000 \text{ A}$   
 $iv10 = 2 * iv5 = -0.064000 \text{ A}$   
 $iv11 = -iv2 - iv3 + iv4 = -0.242000 \text{ A}$

Figure 5: The branch voltages (left) and the branch currents (right) for circuit having dependent sources

## References

- [1] R.C.Dorf, J.A.Svoboda: *Introduction to Electric Circuits*. John Wiley & Sons, Inc., 2007.
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- [3] MATLAB - User's Guide, MathWorks 2009.

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